

International Technology
Roadmap for Semiconductors
Modeling & Simulation Working Group

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ITWG Members

- N. Kotani, SELETE
- A. v. Schwerin, Siemens
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- P. Leon, Intel
- D. Wollesen, AMD (RCG rep)
- R. Goossens, NSC (would like to be replaced)

Issues for '98 Roadmap

- Origin of Technology Requirements numbers should be better documented
- More coverage of non-logic devices (but where do we draw the line? BJT? TFT? LCD?)
- More coverage of compact circuit and interconnect models
- Refinement and expansion of tables
 - possibly publish accompanying web document

Modeling the Roadmap

- Modeling future technologies would serve several purposes:
 - self-consistency of Roadmap
 - more clearly identify future technology issues
 - allow alternatives to be compared
- Best approach: SRC or SEMATECH could fund a university (using widely available tools)
 - make available simulation templates for those wanting to explore/compare alternatives