

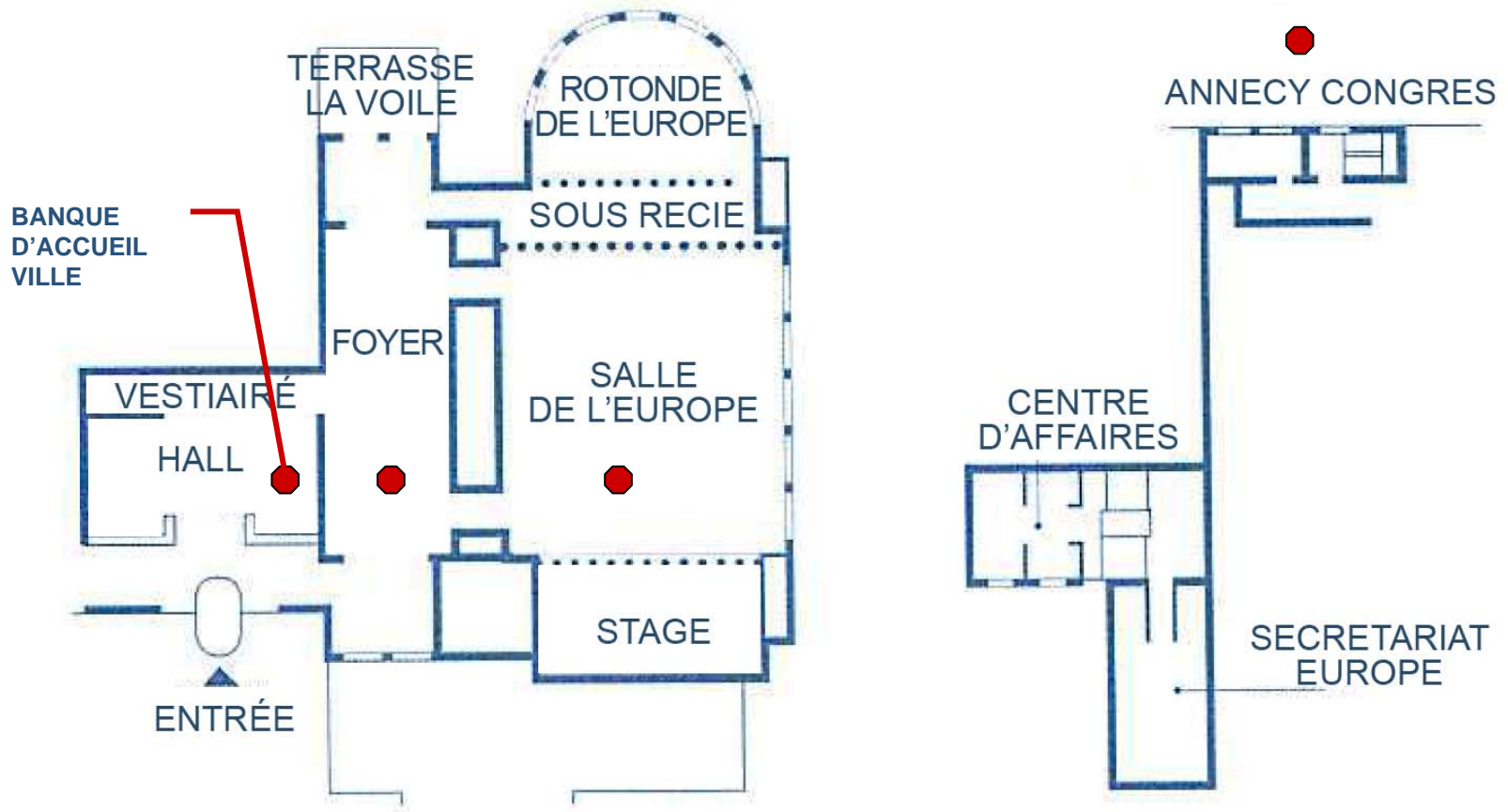
ITRS IRC/ITWG 2007 Spring Workshop and Conference



Meeting
Handout



April 22 Reception Location and April 22-23 Registration – Imperial Palace

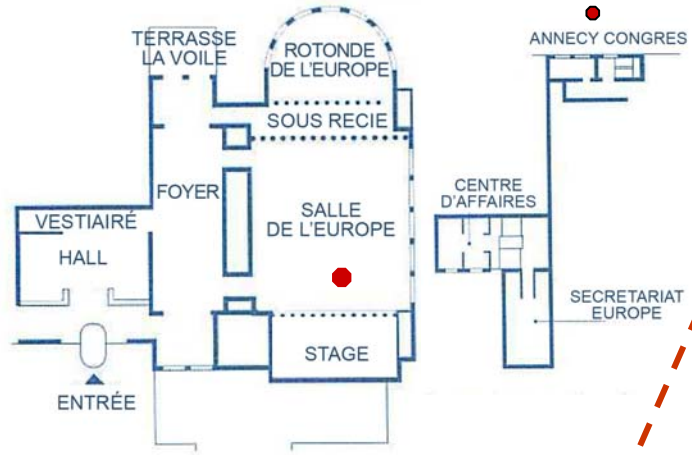


Niveau 1 (Level 1)

Registration –	Banque d'Accueil Ville
April 22 Reception –	Foyer
April 23-24 Plenary [and PIDS] –	Salle de L'Europe
April 23-24 ERM –	AnneCy Congres

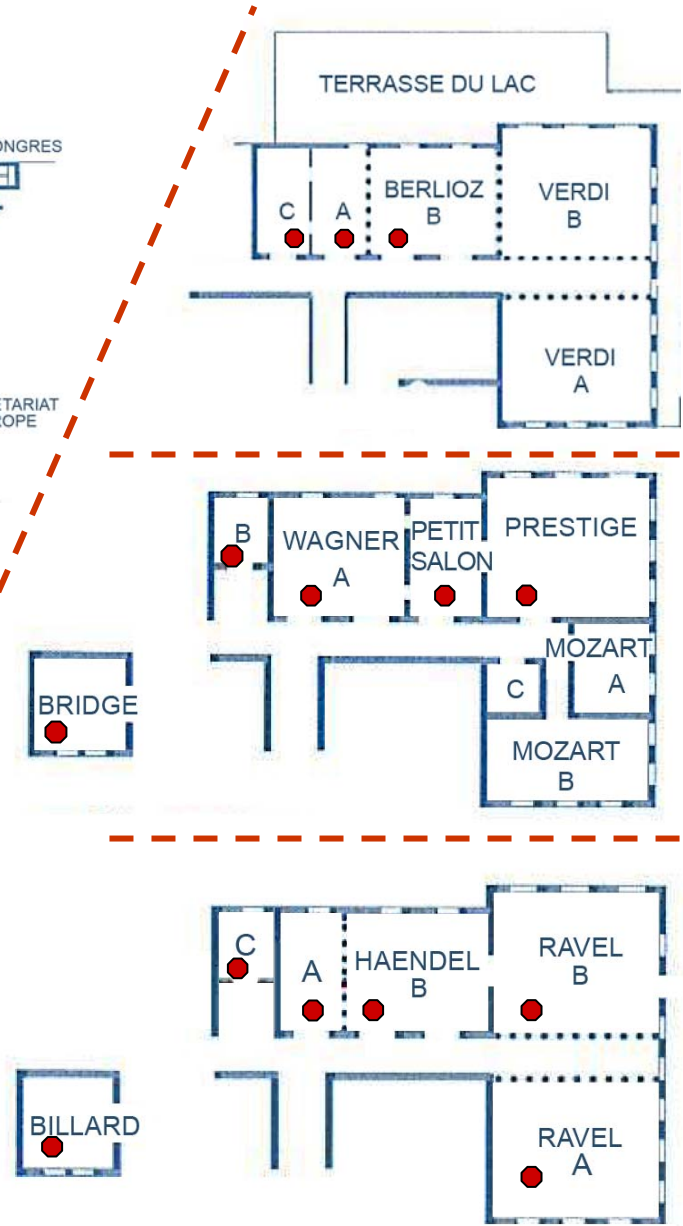


ITWG/IRC Room Assignments – Imperial Palace



Niveau 1 (Level 1)

- Plenary** – Salle de L'Europe
- PIDS** – Salle de L'Europe
- ERM** – Anancy Congres



Niveau 3 (Level 3)

- WECC [Yield]** – Berlioz A
- Factory** – Berlioz B
- Yield** – Berlioz C

Niveau 4 (Level 4)

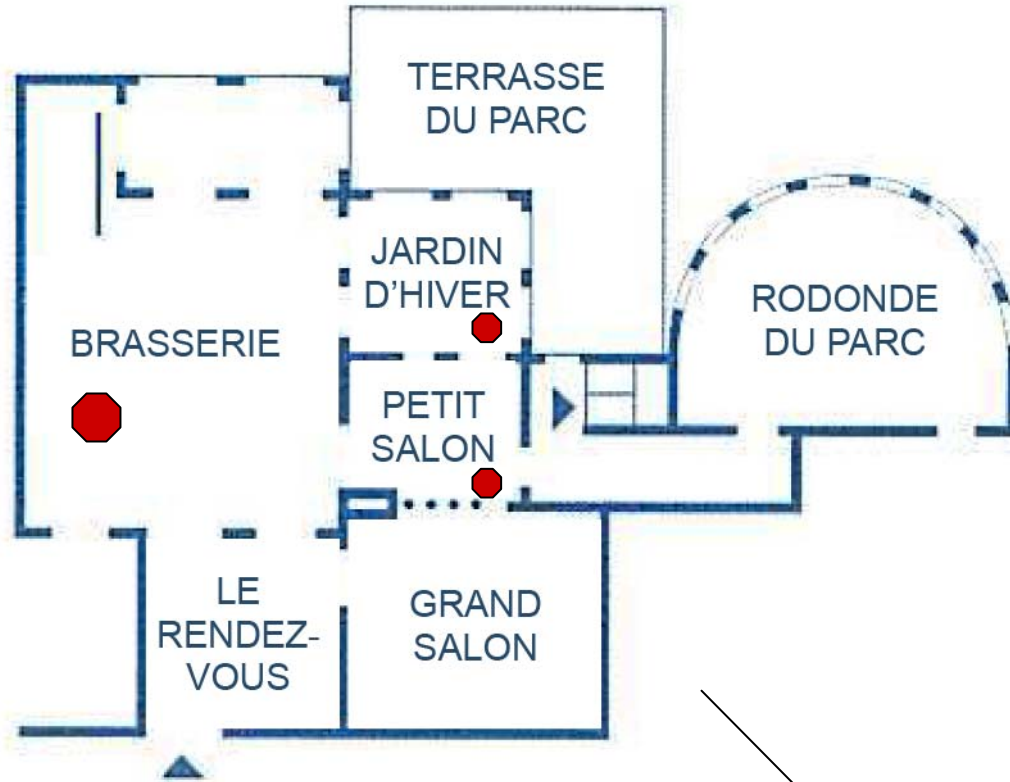
- IRC** – Prestige
- Modeling&Sim.** – Petit Salon
- Assy&Pkg** – Wagner A
- Interconnect** – Wagner B
- Wireless** – Bridge

Niveau 5 (Level 5)

- Metrology** – Haendel A
- Front end Processes** – Haendel B
- ESH** – Haendel C
- Test** – Billard
- Design** – Ravel A
- Lithography** – Ravel B

Meals Locations – Imperial Palace

**ITWG/IRC
Lunches
&
ITRS
Conference
Lunch**



**Rez-de-Parc
(Park Level)**



- **Sunday, April 22nd – Imperial Palace**
 - 17:00-18:00 Registration
 - 18:00 – 20:00 Welcome buffet (registration continues)

- **Monday, April 23rd – Imperial Palace (except for evening event)**
 - 8:30 – 9:00 Registration
 - 9:00 - 10:00 Plenary Session
 - Paolo's introduction
 - Overview of the agenda of the two days (Linda)
 - ORTC (Alan)
 - 10:00 – 12:30 IRC/ITWG Breakouts
 - Coffee break available from 10:00 to 10:30
 - 12:30 – 14:00 Lunch
 - 14:00 – 14:45 Plenary presentation : From R&D to R-I-D, by Prof. Weil
 - 14:45 – 17:45 IRC/ITWG Breakouts
 - Coffee break available from 16:00 to 16:30
 - Includes IRC-selected TWG breakout session on More than Moore, moderated by Prof. Weil & Le Masson, from 14:45 to 16:00
 - 17:45 Adjourn
 - 18:30 – 23:30 Evening event
 - 18:30 – 20:30 Guided tour of old town
 - 23:00 – 23:30 Dinner in Annecy castle



Spring 2007 ITRS ITWG/IRC Meeting
Agenda
April 24, 2007

- **Tuesday, April 24th – Imperial Palace**
 - **9:00 - 12:30** **IRC/ITWG Breakouts**
 - **Coffee break available from 10:00 to 10:30**
 - **12:30 – 14:00** **Lunch**
 - **14:00 – 16:00** **IRC/ITWG Breakouts**
 - **16:00 – 16:30** **Coffee break**
 - **16:30 – 18:30** **Plenary session**
 - **Key messages from all ITWGs**
 - **18:30** **Adjourn**



April 23 Breakout Schedule: IRC, Factory Int., Modeling & Simulation, Assembly & Packaging, Lithography

Day 1 ITWG/IRC Meetings					
	IRC Team Meetings	Factory Int. Team Meetings	Modeling Team Meetings	Assy&Pkg Team Meetings	Lithography Team Meetings
8:30-9:00	Registration	Registration	Registration	Registration	Registration
9:00-10:00	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]
10:00-10:30	Coffee Break				
10:30-11:30	IRC	Factory	Modeling	Assy&Pkg	Lithography
11:30-12:30	IRC	Factory	[meet in Design area]	Assy&Pkg / ESH	Lithography / ERM
12:30-14:00	Lunch				
14:00-14:45	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil
14:45-15:30	In Plenary Room MtM Meeting with IRC/ 6 TWGs and Weil/Le Masson 14:45-16:00	Factory	[meet in FEP area]	In Plenary Room MtM Meeting with IRC/ 6 TWGs and Weil/Le Masson 14:45-16:00	Lithography
15:30-16:30	IRC	Factory	Modeling / ERM	Assy&Pkg	Lithography
16:00-16:30 Coffee Break Available					
16:30-17:00	IRC	Factory	Modeling	Assy&Pkg / Test	In Plenary Room Meeting with Litho/FEP/ Design/PIDS
17:00-17:45	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]
18:30 - 23:30	Evening Event: 18:30-20:30 Guided Tour; 20:30 - 23:30 Dinner in Anecny Castle!				

April 24 Breakout Schedule: IRC, Factory Int., Modeling & Simulation, Assembly & Packaging, Lithography

Day 2 ITWG/IRC Meetings					
	IRC Team Meetings	Factory Int. Team Meetings	Modeling Team Meetings	Assy&Pkg Team Meetings	Lithography Team Meetings
9:00-10:00	IRC	Factory	Modeling	Assy&Pkg	Lithography
10:00-10:30 Coffee Break is Available					
10:30-11:15	IRC	Factory	Modeling	Assy&Pkg	Lithography
11:15-12:00	IRC	Factory	Modeling	Assy&Pkg / Interconnect	Lithography
12:00-12:30	IRC	Factory	Modeling / Interconnect	Assy&Pkg / ERM	Lithography / Yield
12:30-14:00	Lunch				
14:00-14:45	IRC	Factory / FEP	Modeling / PIDS	Assy&Pkg	Lithography / Metrology
14:45-15:30	IRC	Factory / Metrology	Modeling	[meet in Design area]	Lithography / ESH
15:30-16:00	IRC	Factory / Litho	Modeling	[meet in RF/AMS Wireless area]	[meet in Factory area]
16:00-16:30	IRC	Factory/ESH	[meet in Litho area]	Assy&Pkg	Lithography / Modeling
16:00-16:30 Coffee Break Available					
16:30-17:00	IRC	Factory/ Yield	Modeling	Assy&Pkg	Lithography
17:00-18:30	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]
18:30	Adjourn, [dinner is not provided]				

April 23 Breakout Schedule: Front End Processes, Wireless, ESH, Interconnect, Metrology

Day 1 ITWG/IRC Meetings					
	FEP Team Meetings	RF/AMS Wireless Team Meetings	ESH Team Meetings	Interconnect Team Meetings	Metrology Team Meetings
8:30-9:00	Registration	Registration	Registration	Registration	Registration
9:00-10:00	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]
10:00-10:30	Coffee Break				
10:30-11:30	FEP	RF/AMS Wireless	ESH	Interconnect	Metrology
11:30-12:30	FEP	RF/AMS Wireless	[meet in Assy&Pkg area]	Interconnect	Metrology
12:30-14:00	Lunch				
14:00-14:45	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil
14:45-15:30	FEP / Modeling	In Plenary Room MtM Meeting with IRC/ 6 TWGs and Weil/Le Masson 14:45-16:00	[meet in ERM area]	In Plenary Room MtM Meeting with IRC/ 6 TWGs and Weil/Le Masson 14:45-16:00	Metrology
15:30-16:30	FEP	RF/AMS Wireless	ESH	Interconnect	Metrology
16:00-16:30 Coffee Break Available					
16:30-17:00	In Plenary Room Meeting with Litho/FEP/ Design/PIDS	RF/AMS Wireless	ESH	Interconnect/ ERM	Metrology / Yield
17:00-17:45	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]
18:30 - 23:30	Evening Event: 18:30-20:30 Guided Tour; 20:30 - 23:30 Dinner in Ancey Castle!				

April 24 Breakout Schedule: Front End Processes, Wireless, ESH, Interconnect, Metrology

Day 2 ITWG/IRC Meetings					
	FEP Team Meetings	RF/AMS Wireless Team Meetings	ESH Team Meetings	Interconnect Team Meetings	Metrology Team Meetings
9:00-10:00	FEP	RF/AMS Wireless	ESH	Interconnect	Metrology
10:00-10:30 Coffee Break is Available					
10:30-11:15	FEP/ ERM	RF/AMS Wireless	ESH/Yield	Interconnect	Metrology
11:15-12:00	FEP/Yield	RF/AMS Wireless	ESH	Interconnect meet in Assy&Pkg area	Metrology
12:00-12:30	FEP/Metrology	RF/AMS Wireless	ESH	[meet in Modeling area]	[meet in FEP area]
12:30-14:00	Lunch				
14:00-14:45	[meet in Factory area]	Wireless / Design	[meet in Interconnect area]	Interconnect / ESH	[meet in Litho area]
14:45-15:30	FEP/ Interconnect	Wireless / PIDS	[meet in Litho area]	[meet in FEP area]	[meet in Factory area]
15:30-16:00	FEP / ESH	Wireless / Assy&Pkg	[meet in FEP area]	[meet in Metrology area]	Metrology / Interconnect
16:00-16:30	FEP	[meet in Test area]	[meet in Factory]	Interconnect	Metrology / ERD/ERM
16:00-16:30 Coffee Break is Available					
16:30-17:00	FEP	RF/AMS Wireless	ESH	Interconnect	Metrology
17:00-18:30	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]
18:30	Adjourn, [dinner is not provided]				

April 23 Breakout Schedule: Process Integration, Yield Enhancement, Design/System Drivers, Test, ERM

Day 1 ITWG/IRC Meetings					
	PIDS Team Meetings	Yield Enhnc. Team Meetings	Design Team Meetings	Test Team Meetings	ERM Team Meetings
8:30-9:00	Registration	Registration	Registration	Registration	Registration
9:00-10:00	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]
10:00-10:30	Coffee Break				
10:30-11:30	PIDS	Yield	Design	Test	ERM
11:30-12:30	PIDS	Yield	Design / Modeling	Test	Meet in Litho area
12:30-14:00					
14:00-14:45	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil	Plenary Presentation "From R&D to R-I-D" by Prof Weil
14:45-15:30	In Plenary Room MtM Meeting with IRC/ 6 TWGs and Weil/Le Masson 14:45-16:00	Yield	In Plenary Room MtM Meeting with IRC/ 6 TWGs and Weil/Le Masson 14:45-16:00	Test	ERM/ESH
15:30-16:30	PIDS	Yield	Design	Test	Meet in Modeling area
16:00-16:30 Coffee Break Available					
16:30-17:00	In Plenary Room Meeting with Litho/FEP/ Design/PIDS	[meet in Metrology area]	In Plenary Room Meeting with Litho/FEP/ Design/PIDS	[meet in Assy&Pkg area]	Meet in Interconnect area
17:00-17:45	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]
18:30 - 23:30	Evening Event: 18:30-20:30 Guided Tour; 20:30 - 23:30 Dinner in Annecy Castle!				

April 24 Breakout Schedule: Process Integration, Yield Enhancement, Design/System Drivers, Test, ERM

Day 2 ITWG/IRC Meetings					
	PIDS Team Meetings	Yield Enhnc. Team Meetings	Design Team Meetings	Test Team Meetings	ERM Team Meetings
9:00-10:00	PIDS	Yield	Design	Test	ERM
10:00-10:30 Coffee Break is Available					
10:30-11:15	PIDS	[meet in ESH area]	Design	Test	Meet in FEP area
11:15-12:00	PIDS	[meet in FEP area]	Design / Test	[meet in Design area]	ERM
12:00-12:30	PIDS / Design	[meet in Litho area]	[meet in PIDS area]	Test	ERM meet in Assy&Pkg area
12:30-14:00	Lunch				
14:00-14:45	[meet in Modeling area]	Yield	[meet in Wireless area]	Test	ERM
14:45-15:30	[meet in Wireless area]	Yield	Design / Assy&Pkg	Test	ERM
15:30-16:00	PIDS	[meet in Design area]	Design / Yield	Test	ERM
16:00-16:30	[meet in Yield area]	Yield/PIDS	Design	Test / Wireless	Meet in Metrology area
16:00-16:30 Coffee Break is Available					
16:30-17:00	PIDS	Yield (meet in Factory area)	Design	Test	ERM
17:00-18:30	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]	[at Plenary]
18:30	Adjourn, [dinner is not provided]				

Spring 2007 ITRS Public Conference
Wednesday, April 25th – Imperial Palace
Draft Agenda -- Morning

2007 ITRS Spring Conference
25 April
Annecy, France

8:00 - 8:45 Registration

ITRS STATUS AND MAIN WORK AXES FOR 2007, PUSHKAR APTE, CHAIR

8:45 - 9:00	Welcome	Patrick Cogez and Paolo Gargini
9:00 – 9:30	IRC Overview of Key Messages	Paolo Gargini, Mart Graef, Hidemi Ishiuchi
9:30 – 9:50	Overall Roadmap Technology Characteristics	Alan Allan
9:50 – 10:00	Question and Answer	Pushkar Apte, Chair
10:00 – 10:30	Coffee Break	

ITRS STATUS AND MAIN WORK AXES FOR 2007, TOSHIHIKO OSADA, CHAIR

10:30 – 11:00	Emerging Research Devices /Emerging Research Materials	Jim Hutchby/ Mike Garner/Dan Herr
11:00 – 11:20	Process Integration, Devices, and Structures	Peter Zeitzoff
11:20 – 11:40	RF and Analog/Mixed-signal for Wireless Technologies	Margaret Huang
11:40 – 12:00	Assembly and Packaging	Bill Bottoms
12:00 – 12:20	Design	Juan-antonio Carballo
12:20 – 12:30	Question and Answer and Wrap up	Toshihiko Osada, Chair
12:30-14:00	Lunch	



Spring 2007 ITRS Public Conference
Wednesday, April 25th – Imperial Palace
Draft Agenda -- Afternoon

2007 ITRS Spring Conference
25 April
Annecy, France

12:30-14:00 Lunch

EUROPEAN PERSPECTIVE ON THE SEMICONDUCTOR ROADMAP, MART GRAEF, CHAIR

14:00 – 14:30	ENIAC	Dominique Thomas
14:30 – 15:00	ESIA	Martin Spat
15:00 - 15:10	Question and Answer session	Mart Graef, Chair
15:10 – 15:30	Coffee Break	

EUROPEAN SEMICONDUCTOR RESEARCH TRENDS, WOLFGANG ARDEN, CHAIR

15:30 – 16:00	Fraunhofer Center Nanoelectronic Technologies	Peter Kücher
16:00 – 16:30	IMEC	Luc Van Den Hove
16:30 – 17:00	LETI	Michel Brillouët
17:00 – 17:30	Q&A and Conference Wrap Up	Wolfgang Arden, Chair

