

Agenda

Tuesday, November 30, 1999

8:00	Registration (Opening)	M. Iiri	1:15	(Session 2) Front End Processes	K. H. Baik S. Kawamura
9:00	Opening Remarks & Orientation	A. Morino & IRC Members	1:45 2:15 2:45	Interconnect Lithography Factory Integration	C. Case J. Canning M. Honma & D. Pillai
9:30	Outline of ITRS '99	P. Gargini			
9:55	Break		3:15	Break	
	(Session 1)	W. Arden	3:35	(Session 3) ES & H	Y. T. Loh O. Anzai
10:15	Design	T. Hiwatashi	4:05	Modeling & Simulation	N. Kotani
10:45	Test & Test Equipment	R. Nesbitt	4:35	Metrology	A. Diebold
11:15	Assembly & Packaging	H. Kasuga	5:05	Defect Reduction	M. Godwin
11:45	PIDS	N. Endo	5:35	Break	
				(Closing)	R. Doering
12:15	Lunch Break & Poster Sessions		5:40	Q & A	
			6:05	Closing Remarks	Y. Unno